

EXPELLED PROCEDURE - EXAMINING GROUP 2822

S/N 09/551027

JAN 08 2004

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Wende ~~HARPER~~ Noble Jr. et al.

Examiner: Michael Trinh

Serial No.: 09/551027

Group Art Unit: 2822

Filed: April 17, 2000

Docket No.: 303.379US2

Title: CIRCUIT AND METHOD FOR A FOLDED BIT LINE MEMORY CELL
WITH VERTICAL TRANSISTOR AND TRENCH CAPACITOR

AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In response to the final Office Action mailed September 4, 2003, please amend the application as follows:

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a one-month extension of the period for responding to the Office Action, thereby moving the deadline for response from December 4, 2003 to January 4, 2004 (Sunday).

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